



JPW

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q68647

Fumio MORI

Appln. No.: 10/081,211

Group Art Unit: 3729

Confirmation No.: 4413

Examiner: Minh N. TRINH

Filed: February 25, 2002

For: INTEGRATED CIRCUIT MOUNTING STRUCTURE AND ITS MOUNTING METHOD  
THEREOF

**RESPONSE TO RESTRICTION REQUIREMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 23, 2004 in which the Examiner has required a restriction, Applicant responds by electing Group I, *i.e.*, claims 7-11, drawn to an IC mounting method on first substrate. This election is made without prejudice to the filing of a divisional application directed to the non-elected claims.

Prompt and favorable action on the elected claims is now respectfully requested.

Respectfully submitted,

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WASHINGTON OFFICE

**23373**

CUSTOMER NUMBER

Date: July 20, 2004